

ANNAMACHARYA INSTITUTE OF TECHNOLOGY & SCIENCES (Autonomous) :: RAJAMPET

AN EXCLUSIVE NEWS LETTER OF DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING  
FROM ANTRIX-ECE ASSOCIATION

Reach us at [www.aitrajampet.ac.in/ecedept/index.htm](http://www.aitrajampet.ac.in/ecedept/index.htm)

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**Antrix Newsletter** is an amalgamation of all the events held in the Department of Electronics & Communication Engineering, Annamacharya Institute of Technology & Sciences, Rajampet. This e-News letter is compiled by Antrix-ECE Association with the inputs of the Faculty and Students of the Department. It plays an instrumental role in providing a greater exposure of the achievements accomplished.



## VISION

To offer educational experiences those make the students globally competent, socially responsible and bring in answers to ever-ebbing problems in the field of Electronics & Communication Engineering.

## MISSION

To offer high quality premier education in the field of Electronics & Communication Engineering and to prepare students for professional career and higher studies. To promote excellence in technical research, collaborative activities and positive contributions to society

## Program Educational Objectives (PEOs)

- 1) Work efficiently as Electronics & Communication Engineers, including supportive and leadership roles on Multidisciplinary teams.
- 2) Communicate effectively, recognize and incorporate societal needs and constraints in their professional endeavors with high regard to legal and ethical responsibilities.
- 3) Develop attitude in lifelong learning, applying and adapting new ideas and technologies as their field evolves.

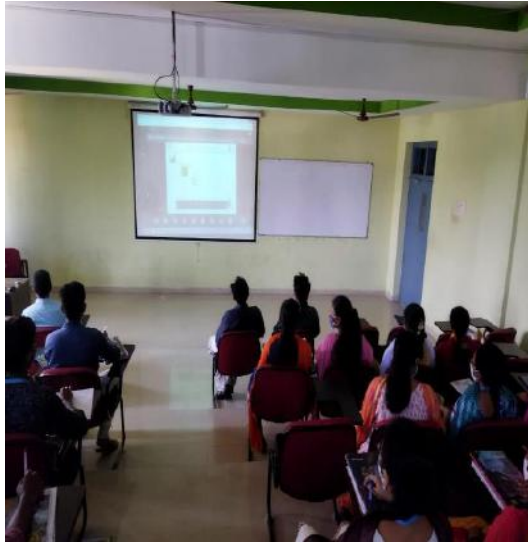
## PROGRAM OUTCOMES

*Students in the Electronic and Communication Engineering program should, at the time of their graduation, are in possession of:*

1. **Engineering knowledge:** Apply the knowledge of mathematics, science, engineering fundamentals, and an engineering specialization to the solution of complex engineering problems.
2. **Problem analysis:** Identify, formulate, review research literature, and analyze complex engineering problems reaching substantiated conclusions using first principles of mathematics, natural sciences, and engineering sciences.
3. **Design/development of solutions:** Design solutions for complex engineering problems and design system components or processes that meet the specified needs with appropriate consideration for the public health and safety, and the cultural, societal, and environmental considerations.
4. **Conduct investigations of complex problems:** Use research-based knowledge and research methods including design of experiments, analysis and interpretation of data, and synthesis of the information to provide valid conclusions.
5. **Modern tool usage:** Create, select, and apply appropriate techniques, resources, and modern engineering and IT tools including prediction and modeling to complex engineering activities with an understanding of the limitations.
6. **The engineer and society:** Apply reasoning informed by the contextual knowledge to assess societal, health, safety, legal and cultural issues and the consequent responsibilities relevant to the professional engineering practice.
7. **Environment and sustainability:** Understand the impact of the professional engineering solutions in societal and environmental contexts, and demonstrate the knowledge of, and need for sustainable development.
8. **Ethics:** Apply ethical principles and commit to professional ethics and responsibilities and norms of the engineering practice.
9. **Individual and team work:** Function effectively as an individual, and as a member or leader in diverse teams, and in multidisciplinary settings.
10. **Communication:** Communicate effectively on complex engineering activities with the engineering community and with society at large, such as, being able to comprehend and write effective reports and design documentation, make effective presentations, and give and receive clear instructions.
11. **Project management and finance:** Demonstrate knowledge and understanding of the engineering and management principles and apply these to one's own work, as a member and leader in a team, to manage projects and in multidisciplinary environments.
12. **Life-long learning:** Recognize the need for, and have the preparation and ability to engage in independent and life-long learning in the broadest context of technological change.

## EVENTS ORGANISED

- One week online short term training Programme on “VLSI Design and Verification” was conducted from March 22<sup>nd</sup> to 27<sup>th</sup> march 2021 by the department of ECE. In this online short term training Programme faculty members are participated and the resource person was Dr. V. Narendra, Assistant professor ECE Department, NIT, Warangal. In this connection resource person explained about VLSI Design and its Verification.



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RAJAMPET, KADAPA DIST. ANDHRA PRADESH

(An AICTE Approved, JNTUA Ananthapuramu, A.P., affiliated & NAAC 'A' accredited Institution)

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING  
Presents

**AICTE Sponsored One Week STTP (Phase -I)**  
on  
**VLSI Design and Verification**  
From 22.03.2021 to 27.03.2021

**e-Certificates for all registered participants as per AICTE norms**  
**No Registration Fee, Limited participants, selected on FCFS basis**  
**Registration Link : <https://tinyurl.com/VLSIDV>**

**Chief Patrons:**  
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**Program Coordinator :** Dr. B. Abdul Rahim, Professor -ECE

**Organizing Secretary :** Dr. N.Bala Dastagiri, Asso. Prof- ECE

**Organizing Committee :** All the faculty members of ECE Department, AITS, Rajampet

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- AICTE one week online short Team training programme on "**VLSI DESIGN AND VERIFICATION**" was conducted on 19th to 24th 2021 in the month of April. This was organised by department of ECE in this faculty and students are participated. On day -1 is Mr. Mahesh mallees happa VLSI Engineer, Golgen light solutions, banglore, he explained About VLSI Design Tools, VLSI Methods and appications. On day 2- the resource person was Dr. R. A. Mishra professor in ECE department, motilar Nehru National Institute of Technology, allahabad. He also explained present and in future applications in "VLSI DESIGNS". On day 3-the resource person was Dr. Shivam Verma Asst. prof. department of ECE, IIT, (BHU) varanesion, he also explained about VLSI devices on spintronic, and different devices was explained to the faculty and students. On day 4- the resource person was Dr. Jaganadha Naidu K.Asst.prof.in department of ECE, VIT, Vellore. He was explained about verification in VLSI Design to the faculty. On day 5-the resource person was Dr. R. S Sakthivas department of ECE, VIT, Vellore, he was explained about memory testing on "VLSI DESIGN" and on the same day Dr. M. Durga prakash department of ECE, Siddart college, Vijayawada, he also explained to the faculty about "CMOS SENSORS". On day 6- the resource person was Dr. P. Sri Hari Rao was explained about fundamentals of analog and mixed signal processing. On day 7-the resource person was Dr. B. T. Krishna, ECE, Dept. JNTUK, he was explained to the faculty and students about "Signal Processing in VLSI", totally this one week online short term training was conducted by different resource persons, they all are explained the VLSI design methods & applications very well to our faculty students.

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING  
 Presents

**AICTE Sponsored One Week STTP (Phase -II)**  
 on  
**VLSI Design and Verification**  
 From 19.04.2021 to 24.04.2021




**e-Certificates for all registered participants as per AICTE norms**  
**No Registration Fee, Limited participants , selected on FCFS basis**  
**Registration Link : <https://tinyurl.com/VDV-II>**

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Department of Electronics and Communication Engineering  
**Presents**

**One week online Webinar Series - 2021 on "Recent Advancements of Soft Computing techniques in Applied Electronics" (RASTE-21)**

From 15.06.2021 to 19.06.2021

url for Registration: <https://tinyurl.com/WebinarSeries-21>  
 E-certificates for all registered Participants,  
 No registration fee

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**Convenor :** Dr.Ch.Nagaraju, Professor & HOD ECE

**Co-Convenor :** Dr.B.Abdul Raheem, Coordinator- Professional Bodies

In Technical Collaboration with ISTE, IEEE Student Branch, AITS, Rajampet

Open with ▼

**Resource Persons**

15-06-2021	Global opportunities in the field of emerging technologies in the era of COVID: Germany-India Context	<b>Mr.V.V.N Raj</b> President German Varsity for Advanced Studies, Germany 
16-06-2021	IoT architecture	<b>Mr.Renjith CV</b> Electrical Architect - Medical Imaging at Philips Healthcare Pune, Maharashtra, India 
17-06-2021	Insights of virtual reality and augmented reality for various applications.	<b>Dr.Martin Sagayam Kulandairaj</b> Karunya University Coimbatore 
18-06-2021	Data analytics for Medical Internet of things.	<b>Dr.G.Rajesh</b> Anna University Chennai 
19-06-2021	Recent and Future trends in VR and AR	<b>Dr.K.Surendheran</b> Department of Multimedia VIT, Vellore, Tamilnadu 

**Chief Coordinators :** Mr.G.Thirumalaiah & Mr.M.Hanumanthu  
**Coordinator :** Mr. Syed Javeed Basha  
**Organizing Convenor :** Dr.Fahimuddin Shaik  
**Organizing Committee:** All the faculty members of ECE, AITS-RJPT  
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- AICTE is sponsored one week online STTP (Phase - III) on "**VLSI DESIGN AND VERIFICATION**" was conducted on 14th to 19th 2021 in the department of ECE, in this connection faculty was attended in online. The resource persons are on this workshops are: On day 1-the resource person was Dr. Sangeetha singh ,Asst.prof. IIT, Patna, she explained about the concept on "CMOS devices and current reasonable trends". On day 2- the resource person was Dr .Tarun Kumar Guptha , NIIT, Bhopal, he explained CMOS logic design. On this day in the afternoon session resource, Dr. Shrotri Suman Assit.prof. KLU, Vijayawada, she explained the concept on "performs, characterisation and applications of voltage controlled ring Oscillation. On day 3- the resource person was Dr. SV Shantmittal Asst. prof. department of ECE , IIT (plto) she explained about the concept on "Transistor Technology in latest modelling device" and also the concept of analog circuits & performances. On day 4-the resource person was Dr. Suresha. AICTE nominated, MARGADARSHAK, he explained the concept on National Educational policy-2020. How to implement the policy 2020 was explained in the session on the same day by Dr. sanjeev Raj Asst. Prof. MNNIT, Allahabad. He was also explained about "NSI design leakage control and low power design". Finally he explained "VLSI DESIGN" concepts very well to students and faculties.



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**AICTE Sponsored One Week STTP (Phase -III)**  
 on  
**VLSI Design and Verification**  
 From 14/06/2021 to 19/06/2021

**e-Certificates for all registered participants as per AICTE norms**  
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**Registration Link : <https://tinyurl.com/VDV-III>**

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**Antrix- ECE Association Events:**

- "Elocution Competition" was conducted for 2nd, 3rd year students of ECE department on 1<sup>st</sup> April 2021 by ANTRIX ECE Association.



## **FACULTY PUBLICATIONS**

### **Journals:**

- **Fahimuddin Shaik**, P.charan kumar, K.lakshmi prasanna, G.jyothi, C.guru Kishore, “Lane Detection For Auto-Pilot vehicles Using Perspective And Histogram Approach”, International Journal of Advanced Trends in Engineering, Science and Technology, Volume: 06, Issue: 03, May 2021, Page No.: 29-33, ISSN : 2456-1126, **DOI:10.22413/ijatest/2021/v6/i3/8**
- **J.Himabindhu**, P.Jagruthi, N.Bharath kumar, A.Dharani, S.Abdul kalam “A Novel Technique For Authentication Using ECG”, International Journal of Advanced Trends in Engineering, Science and Technology, Volume: 06, Issue: 03, May 2021, Page No.: 16-19, ISSN : 2456-1126 , **DOI:10.22413/ijatest/2021/v6/i3/5**
- **M.Ravikishore**, D.Suresh, **N.Baladastagiri**, E.Venkata Lakshmi, G.Sireesha, G.Sandeep, “Analysis of CMOS 45nm Transmission Gate Based Pulsed Latch”, International Journal of Advanced Trends in Engineering, Science and Technology, Volume: 06, Issue: 03, May 2021, Page No.: 16-19, ISSN : 2456-1126 , **DOI:10.22413/ijatest/2021/v6/i3/7**
- S. Arun Prakash, M.G. Sumithra, **K.Shankar**, Amit Grover, Mehtab Singh, Jyoteesh Malhotra, “ Performance investigation of spectral-efficient high-speed inter-satellite optical wireless communication link incorporating polarization division multiplexing”, Optical and Quantum Electronics, Volume:53, Issue:270, May 2021 ISSN: 0306-8919, DOI: <https://doi.org/10.1007/s11082-021-02950-8> (SCI, SCOPUS)

### **International Conferences:**

- B.Khadeej Ul Umerah, M.Hima Bindhu, **Dr.B.Abdul Raheem** “Esafe for Document Protection”, 2<sup>nd</sup> International Multidisciplinary Conference on Information Science, Management Research and Social Science, Alpha Arts and Science College, Chennai, India, 19<sup>th</sup> March 2021.**ISBN:978-93-5457-137-4**

### **SDP/FDP/MDP/Workshops:**

S.No	Name of Faculty	SDP/FDP/MDP/ Workshops	Organized Institution	Date(s)
1.	Dr.N.Baladastagiri	AICTE Sponsored One Week STTP on “Mixed Signal Design Approaches for Artificial Intelligence Processors	Lakkireddy Balireddy College of Engineering, Mylavaram	1 <sup>st</sup> to 6 <sup>th</sup> February 2021



2.	Dr.K.Prasad	AICTE Sponsored One Week STTP on “Mixed Signal Design Approaches for Artificial Intelligence Processors	Lakkireddy Balireddy College of Engineering, Mylavaram	1 <sup>st</sup> to 6 <sup>th</sup> February 2021
3.	B.Prasanthi	Sensors & Technology	Sahakar Maharshi Shankarrao Mohite Patil Institute of Technology and Research and shikshan prasarak mandal Akluj, shankar rao mohite mahavidyalaya,Akluj	10 <sup>th</sup> to 16 <sup>th</sup> March 2021
4.	B.Prasanthi	AICTE Sponsored One Week STTP (Phase - I) on “VLSI Design and Verification”	Annamacharya Institute of Technology and Sciences	22 <sup>nd</sup> to 27 <sup>th</sup> March 2021
5.	Syed Javeed Basha	AICTE Sponsored one week STTP ( Phase-I) on “VLSI Design and Verification”	Annamacharya Institute of Technology and Sciences, Rajampet.	22 <sup>nd</sup> to 27 <sup>th</sup> March 2021
6.	M.Ravikishore	AICTE Sponsored One Week STTP (Phase - I) on “VLSI Design and Verification”	Annamacharya Institute of Technology and Sciences, Rajampet.	22 <sup>nd</sup> to 27 <sup>th</sup> March 2021
7.	Dr. Fahimuddin Shaik	AICTE Sponsored One Week STTP (Phase - I) on “VLSI Design and Verification”	Annamacharya Institute of Technology and Sciences, Rajampet.	22 <sup>nd</sup> to 27 <sup>th</sup> March 2021
8.	G.Obulesu	AICTE Sponsored One Week STTP (Phase - I) on “VLSI Design and Verification”	Annamacharya Institute of Technology and Sciences, Rajampet.	22 <sup>nd</sup> to 27 <sup>th</sup> March 2021
9.	G.Sudhakaran	AICTE Sponsored One Week STTP (Phase - I) on “VLSI Design and Verification”	Annamacharya Institute of Technology and Sciences, Rajampet.	22 <sup>nd</sup> to 27 <sup>th</sup> March 2021
10.	J.Himabindhu	AICTE Sponsored One Week STTP (Phase - I) on “VLSI Design and Verification”	Annamacharya Institute of Technology and Sciences, Rajampet.	22 <sup>nd</sup> to 27 <sup>th</sup> March 2021
11.	Dr.J.Chinnababu	AICTE Sponsored One Week STTP (Phase - I) on “VLSI Design and Verification”	Annamacharya Institute of Technology and Sciences, Rajampet.	22 <sup>nd</sup> to 27 <sup>th</sup> March 2021
12.	K.Giridhar Chaitanya	AICTE Sponsored One Week STTP (Phase - II) on “VLSI Design and Verification	Annamacharya Institute Of Technology And Sciences	19 <sup>th</sup> to 24 <sup>th</sup> April 2021

13.	Syed Javeed Basha	AICTE Sponsored One Week STTP (Phase - II) on "VLSI Design and Verification"	Annamacharya Institute Of Technology And Sciences	19 <sup>th</sup> to 24 <sup>th</sup> April 2021
14.	Dr. Fahimuddin Shaik	AICTE Sponsored One Week STTP (Phase - II) on "VLSI Design and Verification"	Annamacharya Institute Of Technology And Sciences	19 <sup>th</sup> to 24 <sup>th</sup> April 2021
15.	Dr.P.Maniganda	AICTE Sponsored One Week STTP (Phase - II) on "VLSI Design and Verification"	Annamacharya Institute Of Technology And Sciences	19 <sup>th</sup> to 24 <sup>th</sup> April 2021
16.	R.Mahesh Kumar	AICTE Sponsored One Week STTP (Phase - II) on "VLSI Design and Verification"	Annamacharya Institute Of Technology And Sciences	19 <sup>th</sup> to 24 <sup>th</sup> April 2021
17.	G.Thirumalaiah	AICTE Sponsored One Week STTP (Phase - II) on "VLSI Design and Verification"	Annamacharya Institute Of Technology And Sciences	19 <sup>th</sup> to 24 <sup>th</sup> April 2021
18.	Dr.K.Muralibabu	AICTE Sponsored One Week STTP (Phase - II) on "VLSI Design and Verification"	Annamacharya Institute Of Technology And Sciences	19 <sup>th</sup> to 24 <sup>th</sup> April 2021
19.	K.SreenivasaRao	AICTE Sponsored One Week STTP (Phase - I) on "Practical Exposure on Machine Learning & Datasience"	G.Pullareddy Engineering College, Kurnool	26 <sup>th</sup> April to 1 <sup>st</sup> May 2021
20.	Dr.G.Sudhagar	AICTE Sponsored One Week STTP (Phase - I) on "Practical Exposure on Machine Learning & Datasience"	G.Pullareddy Engineering College, Kurnool	26 <sup>th</sup> April to 1 <sup>st</sup> May 2021
21.	Dr.C.Karthik	AICTE Sponsored One Week STTP (Phase - I) on "Practical Exposure on Machine Learning & Datasience"	G.Pullareddy Engineering College, Kurnool	26 <sup>th</sup> April to 1 <sup>st</sup> May 2021
22.	Dr.G.Ramprabhu	AICTE Sponsored One Week STTP (Phase - I) on "Practical Exposure on Machine Learning & Datasience"	G.Pullareddy Engineering College, Kurnool	26 <sup>th</sup> April to 1 <sup>st</sup> May 2021
23.	Dr.CH.Nagaraju	AICTE Sponsored One Week STTP (Phase - I) on "Practical Exposure on Machine Learning & Datasience"	G.Pullareddy Engineering College, Kurnool	26 <sup>th</sup> April to 1 <sup>st</sup> May 2021

## **Patents:**

<b>S.NO</b>	<b>Name of Applicant(s)</b>	<b>Name of other Inventor(s)</b>	<b>Title of the Invention</b>	<b>Application No</b>	<b>Date of Filing</b>	<b>Publication Date</b>
1.	ShaikKarimullah Syed JaveedBasha	ShaikKarimullah Syed JaveedBasha	A Novel Method of Design of Low Power VLSI Based Viterbi Decoder Using Gate Diffusion Input	202131004459	02/02/21	26/02/21
2.	P.Brundavani	P.Brundavani	An Artificial Neural Network System for Functional MRI Segmentation With CC-BPA	2021100880	--	7/04/21

## **Editorial Team**

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Dr.Ch. Nagaraju  
Professor & HOD

### **Guest Editor**

Mr. B. Abdul Rahim  
Mr. Fahimuddin. Shaik

### **Editors**

Mrs. B. Prasanthi  
Ms. V. Sai Anusha